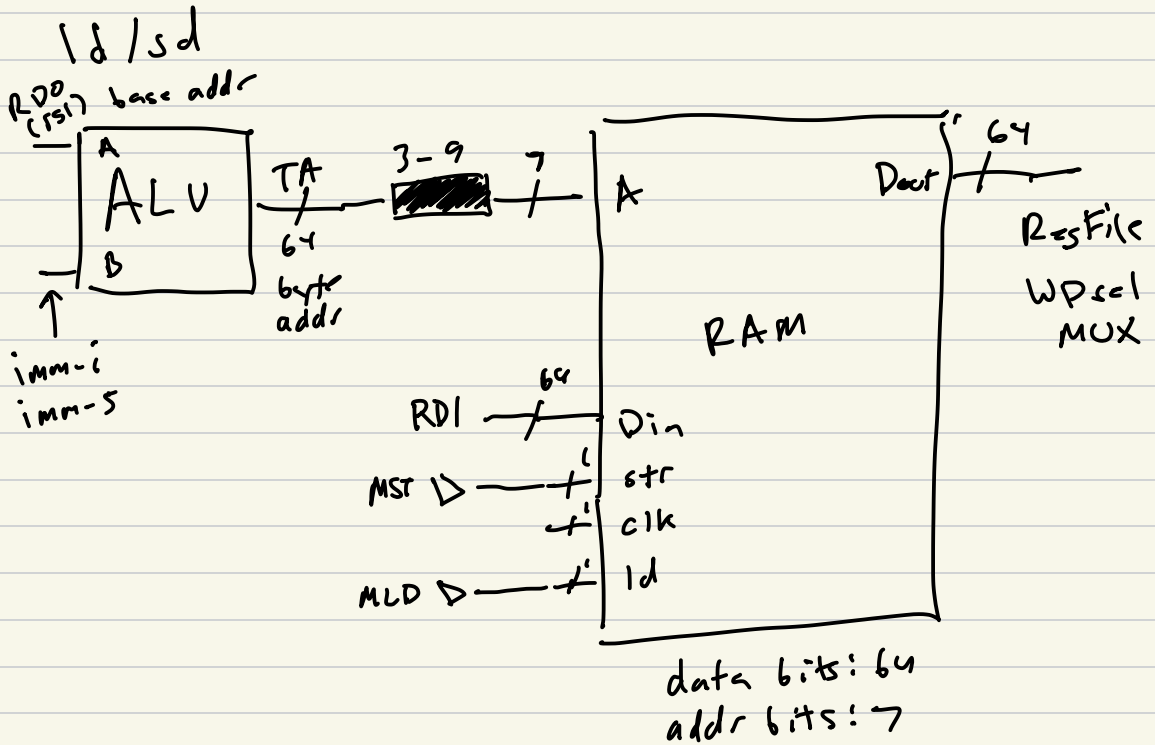
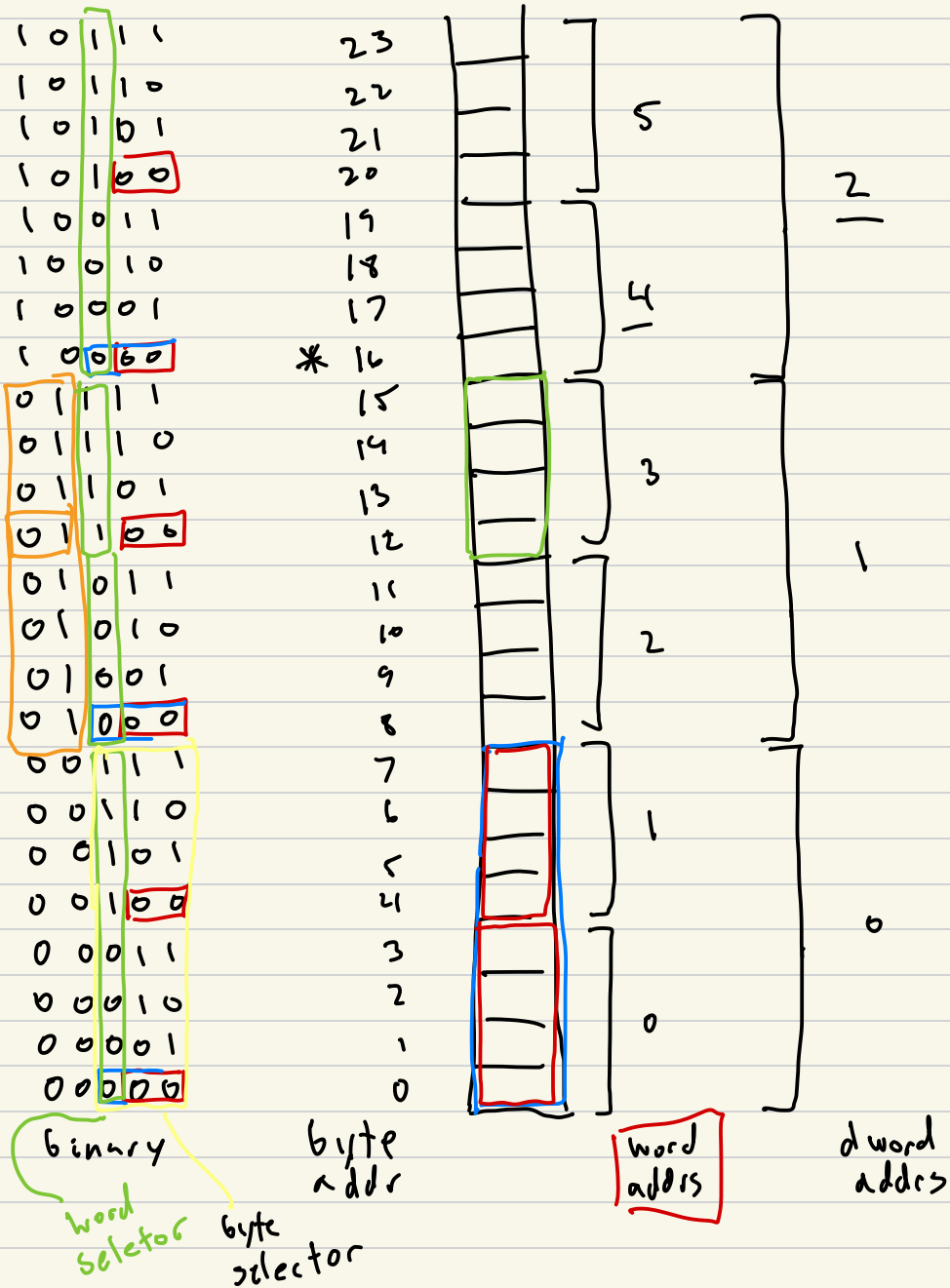


Data Memory ld/sw

lw/sw



Memory layout



lw / sw

